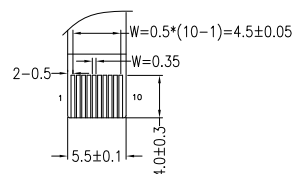
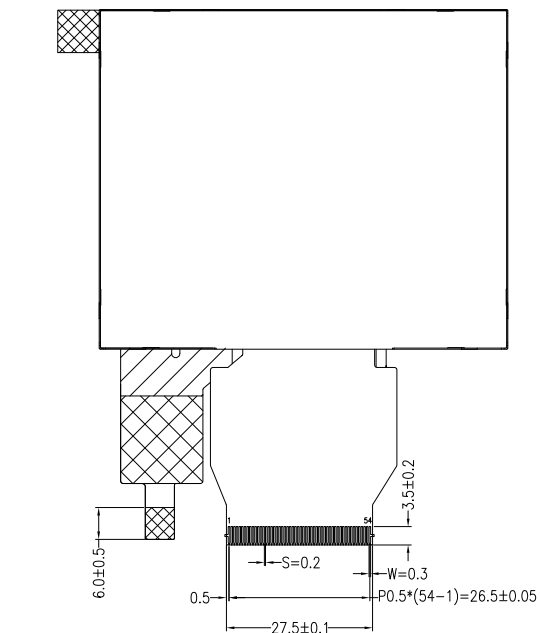


DETAIL A
(SCALE 50:1)



DETAIL B
(SCALE 2:1)



BACKLIGHT CIRCUIT DIAGRAM

PIN DESCRIPTION FOR TFT			
PIN	SYMBOL	PIN	SYMBOL
1	LEDK	28	D16
2	LEDK	29	D17
3	LEDA	30	D18
4	LEDA	31	D19
5	NC	32	D20
6	RESET	33	D21
7	NC	34	D22
8	NC	35	D23
9	NC	36	HSYNC
10	NC	37	VSYNC
11	NC	38	CLK(DOTCLK)
12	D0	39	NC
13	D1	40	NC
14	D2	41	VDD
15	D3	42	VDD
16	D4	43	SPENA(NCS)
17	D5	44	NC
18	D6	45	NC
19	D7	46	NC
20	D8	47	NC
21	D9	48	NC
22	D10	49	SPCK(SCL)
23	D11	50	SPDA(SDI)
24	D12	51	NC
25	D13	52	DEN(ENB)
26	D14	53	GND
27	D15	54	GND

PIN DESCRIPTION FRO CTP	
PIN	SYMBOL
1	VSS
2	VCC
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

NOTES FOR TFT:

1. DISPLAY TYPE: TFT, TRANSMISSIVE, NORMALLY WHITE
2. OPERATING VOLTAGE: VDD=3.3V (TYP.)
3. VIEWING DIRECTION: 12 O'CLOCK
4. DRIVER IC: NV3035C
5. LED BACKLIGHT: 6-CHIP WHITE LED, If=20mA, Vf=19.2V
6. TOP POLARIZER SURFACE TREATMENT: GLARE

NOTES FOR CTP:

1. 3.5 INCH PROJECTIVE CAPACITIVE TOUCH PANEL
2. COVER GLASS + SENSOR GLASS + FPCA
3. OPERATING VOLTAGE: VCC=IOVCC=3.3V
4. DRIVER IC: FT5446DQS; IIC INTERFACE
5. RESOLUTION: 320*240
6. MULTI FINGER: UP TO 5
7. HARDNESS OF COVER SURFACE: 7H
8. FIRMWARE FILE: MI0350CFP-C3 V02 FIRMWARE 20170322

NOTES FOR OTHERS:

1. OPERATING TEMP.: -20°C ~ 70°C
2. STORAGE TEMP.: -30°C ~ 80°C
3. SURFACE LUMINANCE: 420 CD/M² (TYP.)
4. GENERAL TOLERANCE: ±0.2MM
5. ROHS COMPLIANT



MULTI-INNO TECHNOLOGY CO., LTD.

DRAWN BY:	RITA	2017.12.01		MODULE P/N:
CHECKED BY:			SCALE : 1/1	MI0350C1T-16CP
APPROVED BY:			UNIT: mm	DESCRIPTION:
VERSION NO:	01		SHEET NO.: 1/1	TFT + CTP MODULE

01	FIRST ISSUE	RITA	2017.12.01
VER.	REVISED DESCRIPTION	REVISER	DATE